

FIG. 1A
(prior art)

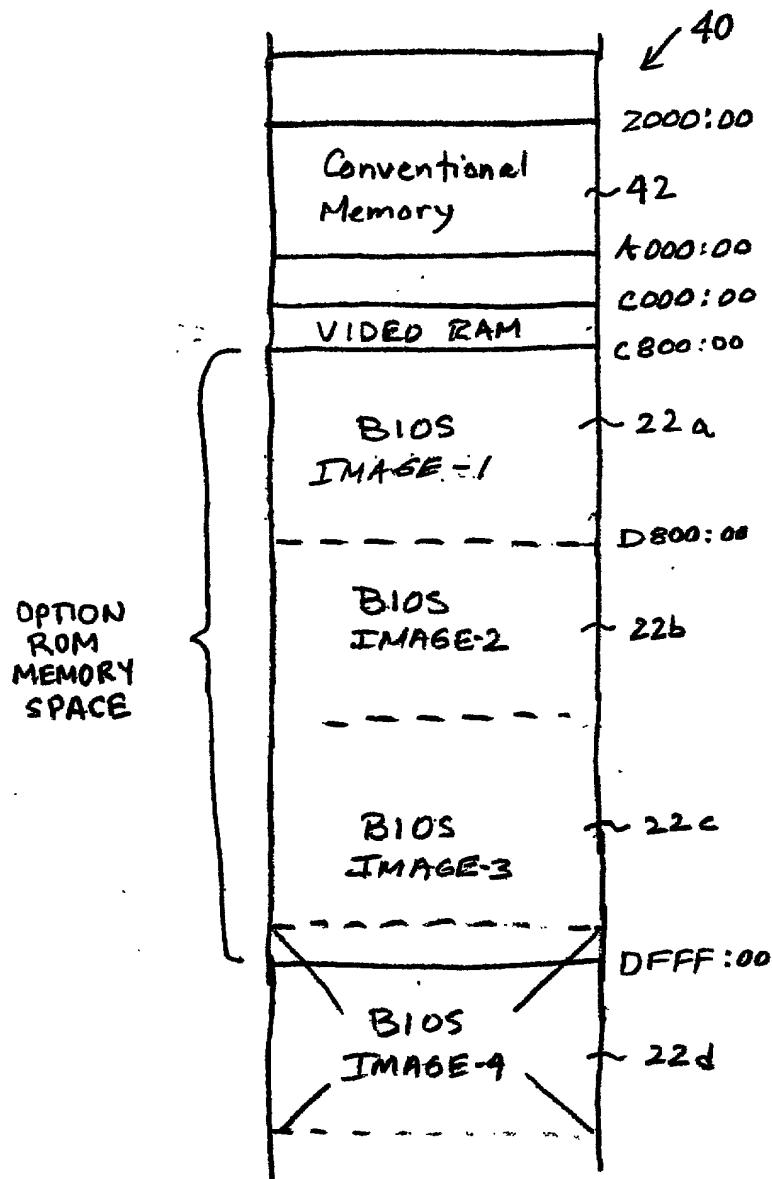


FIG. 1B
(prior art)

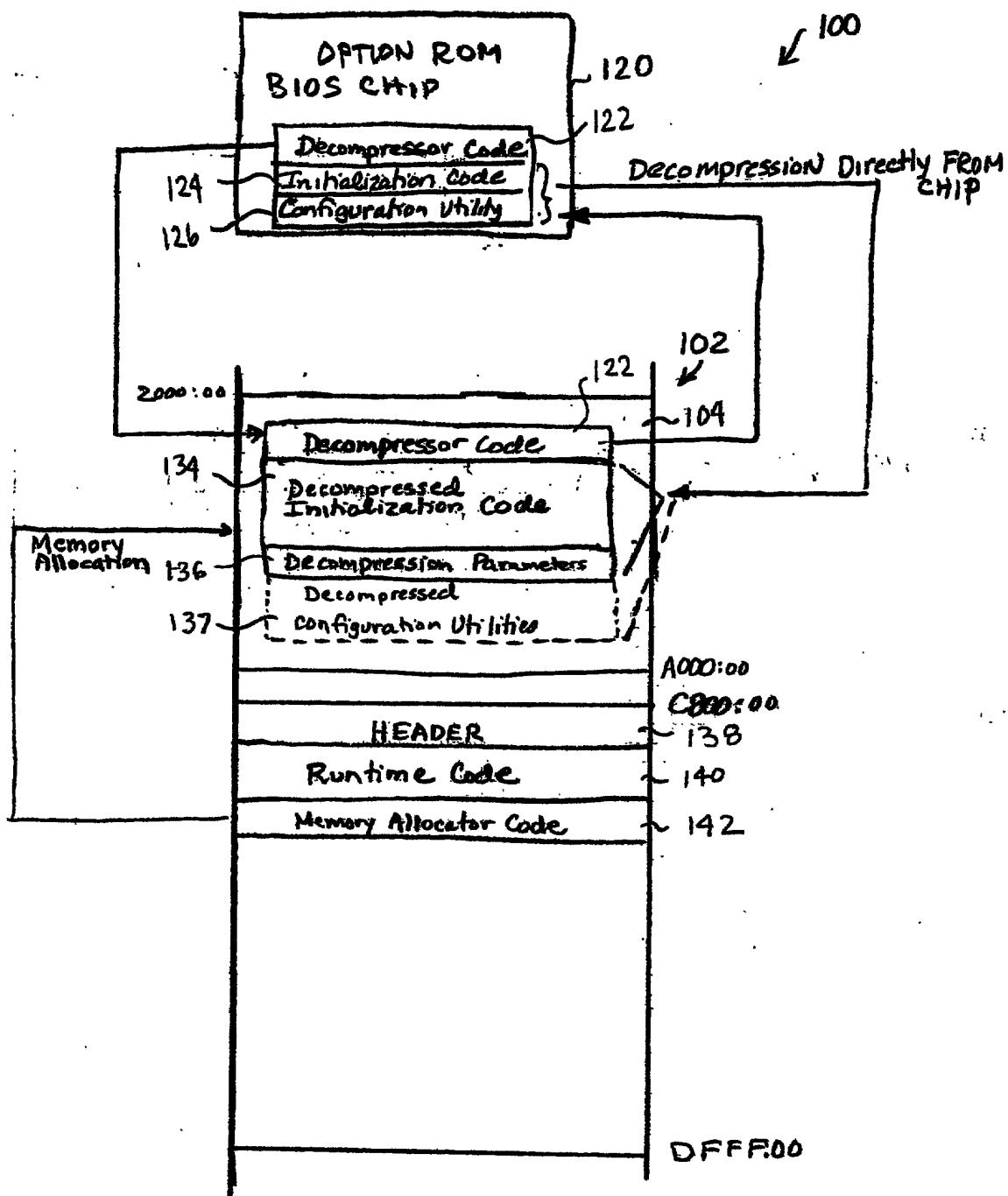


FIG. 2A

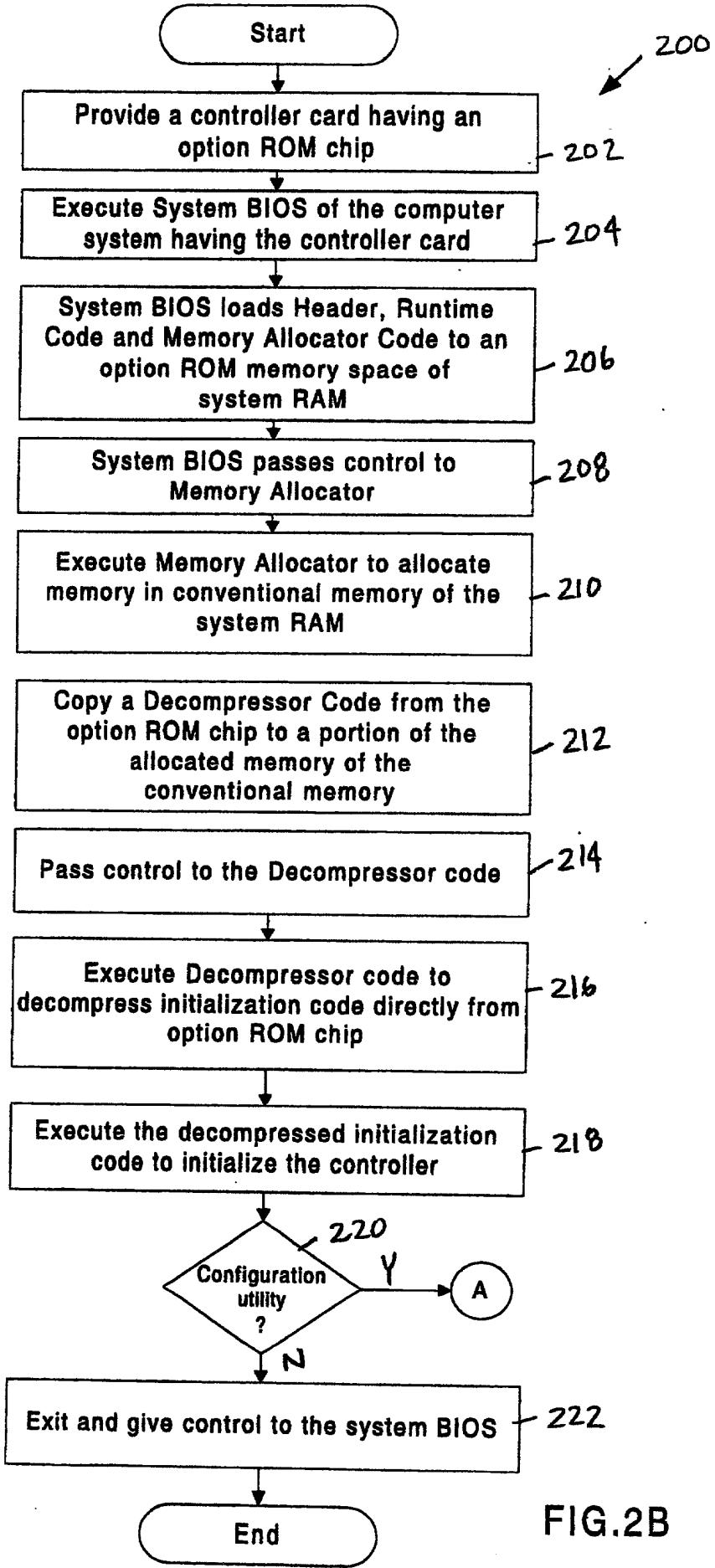


FIG.2B

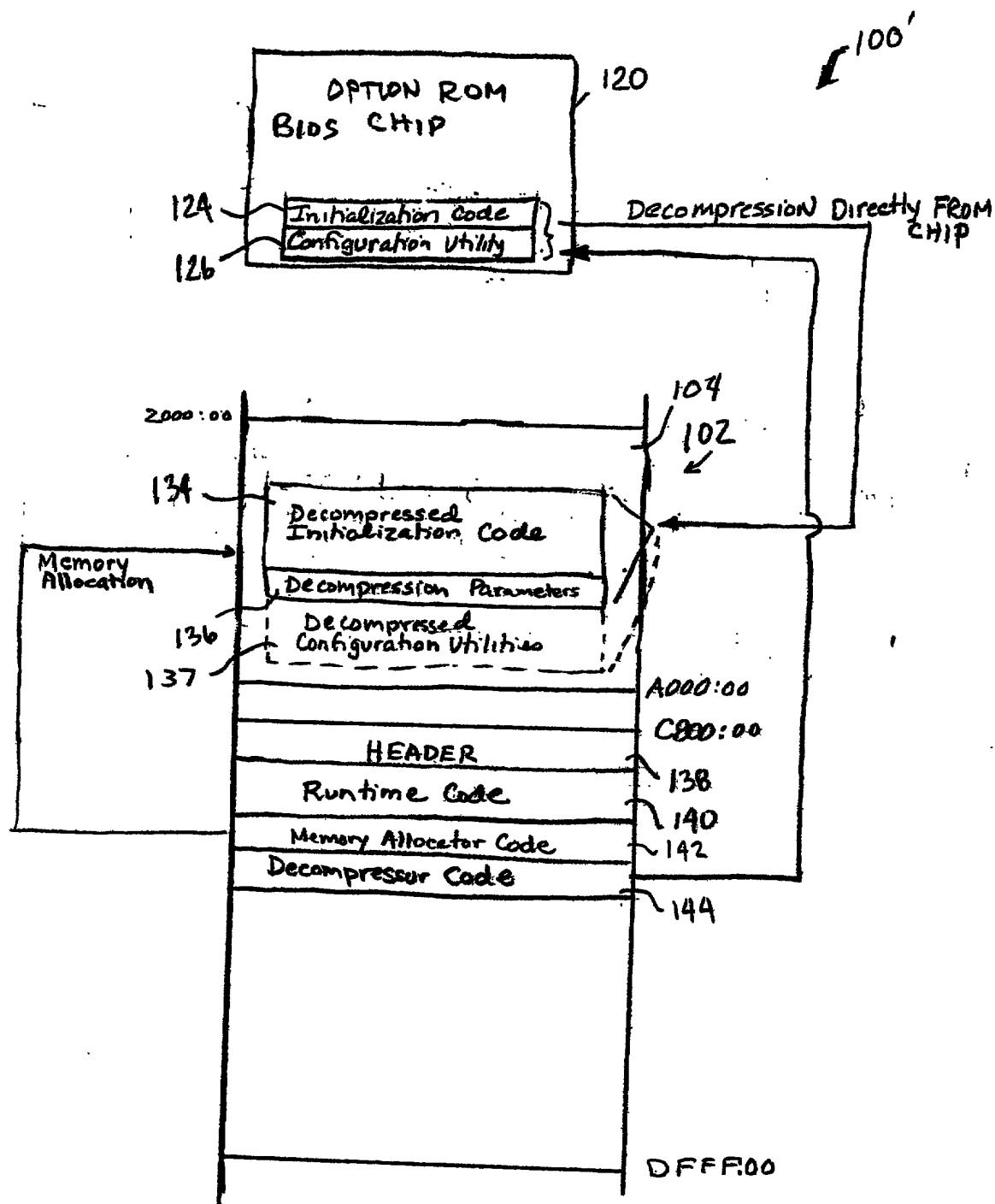


FIG. 3A

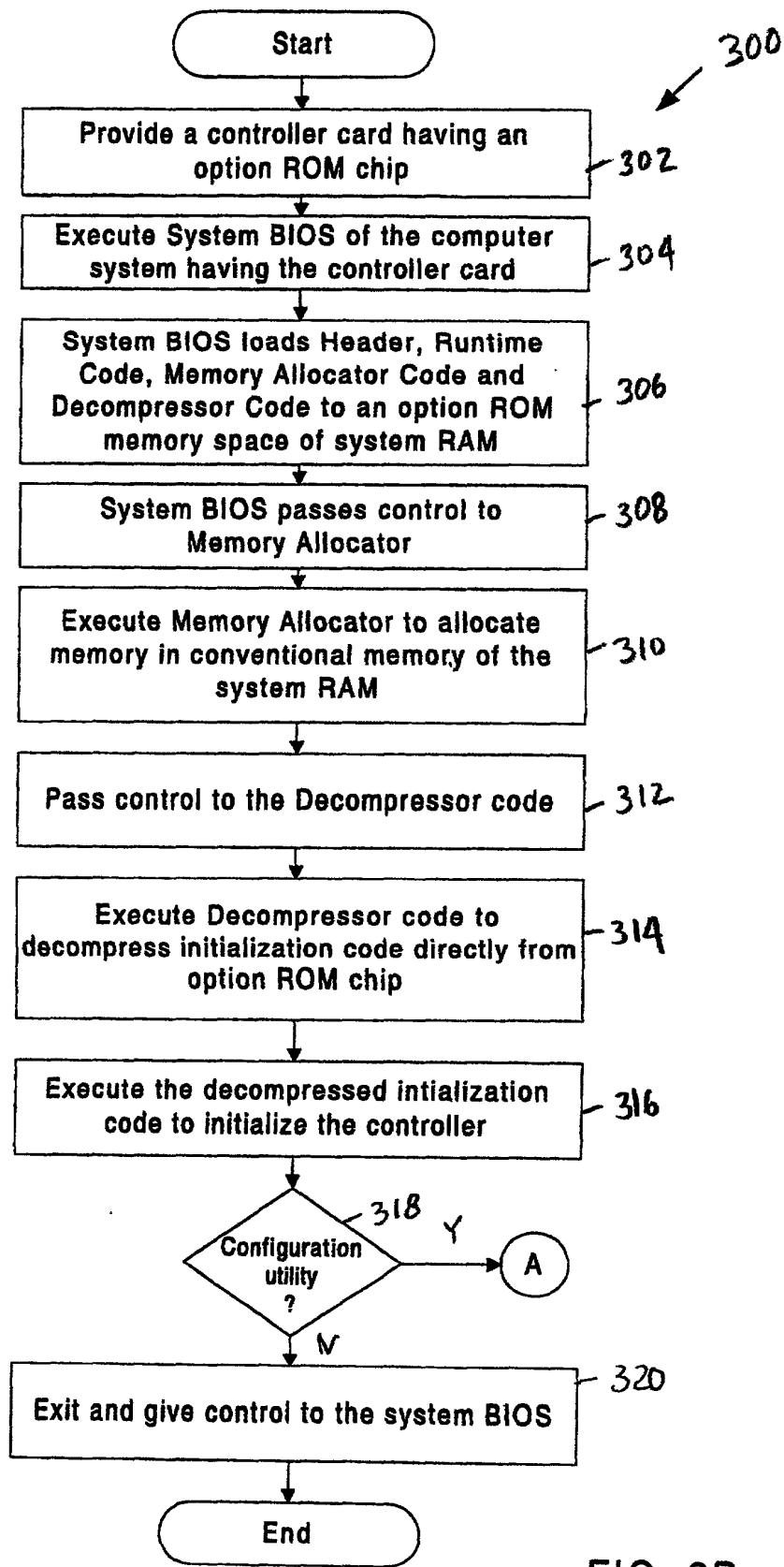


FIG. 3B

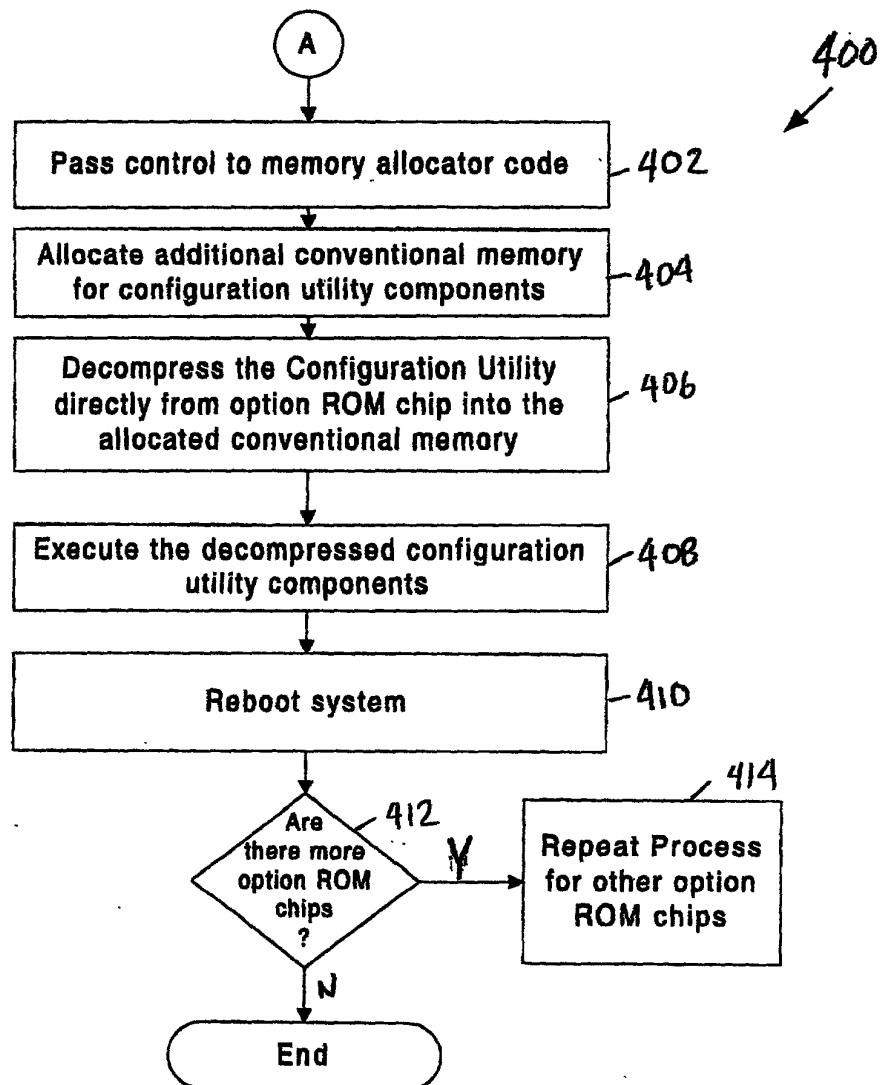
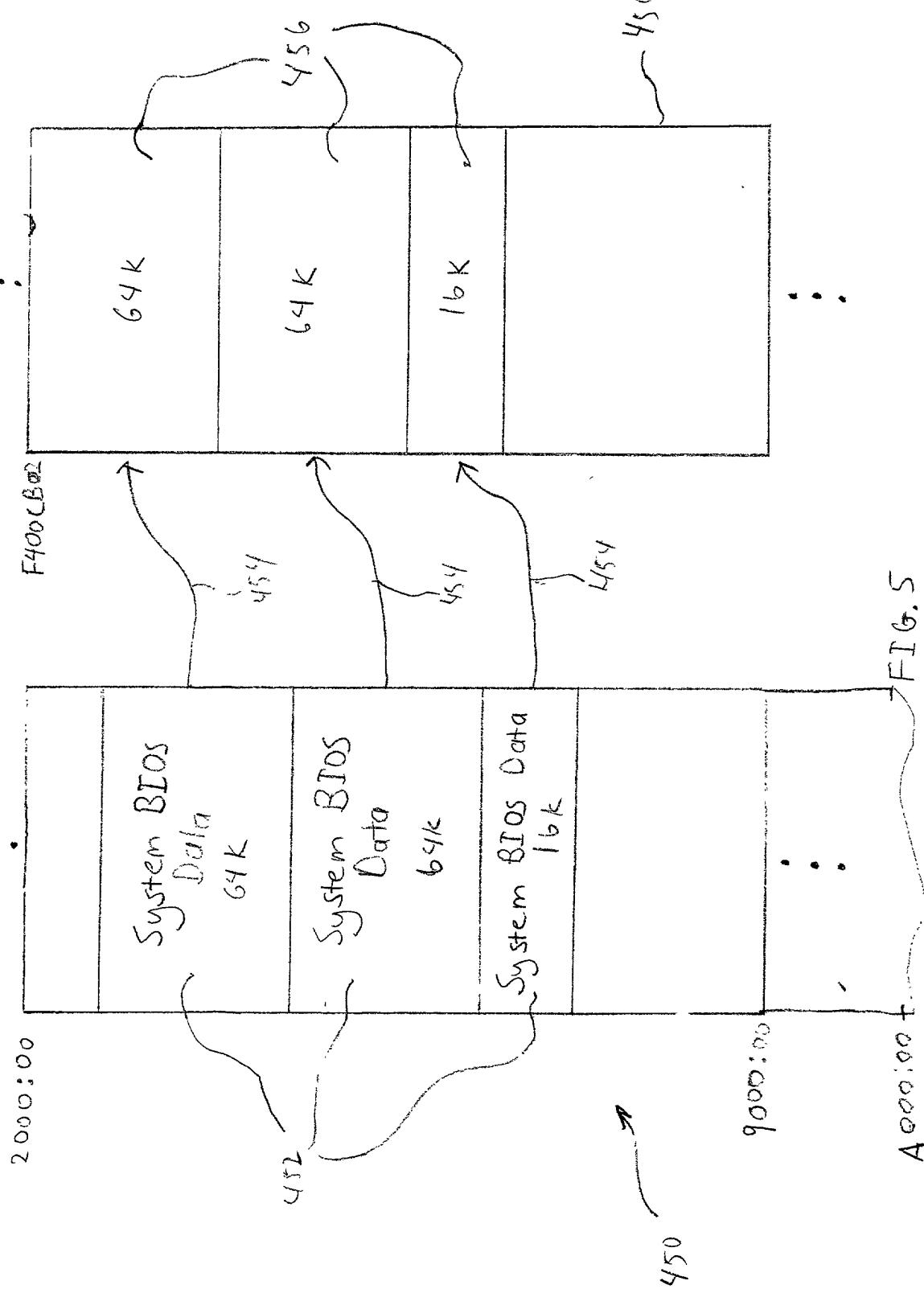


FIG. 4

Conventional Memory



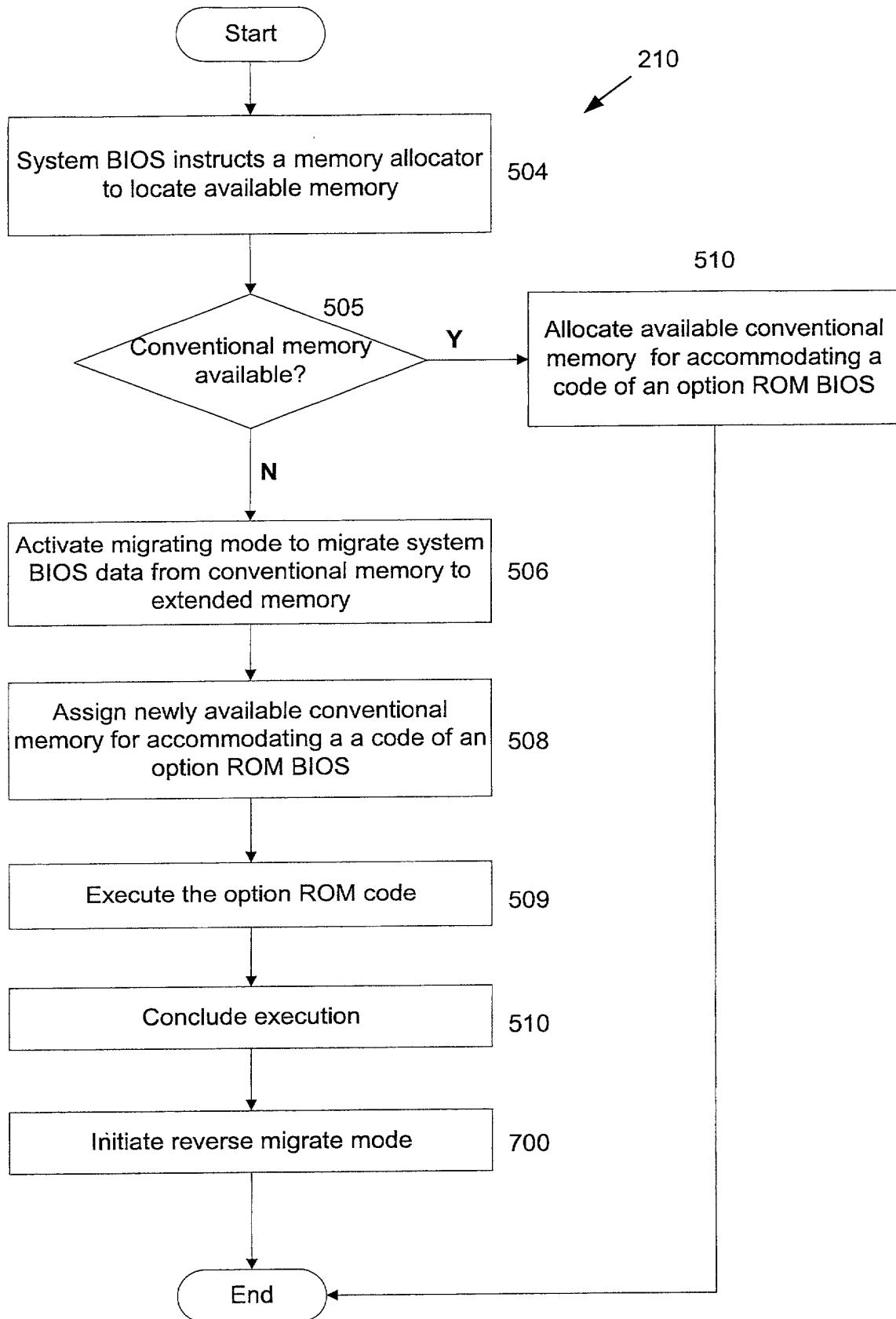


FIG. 6

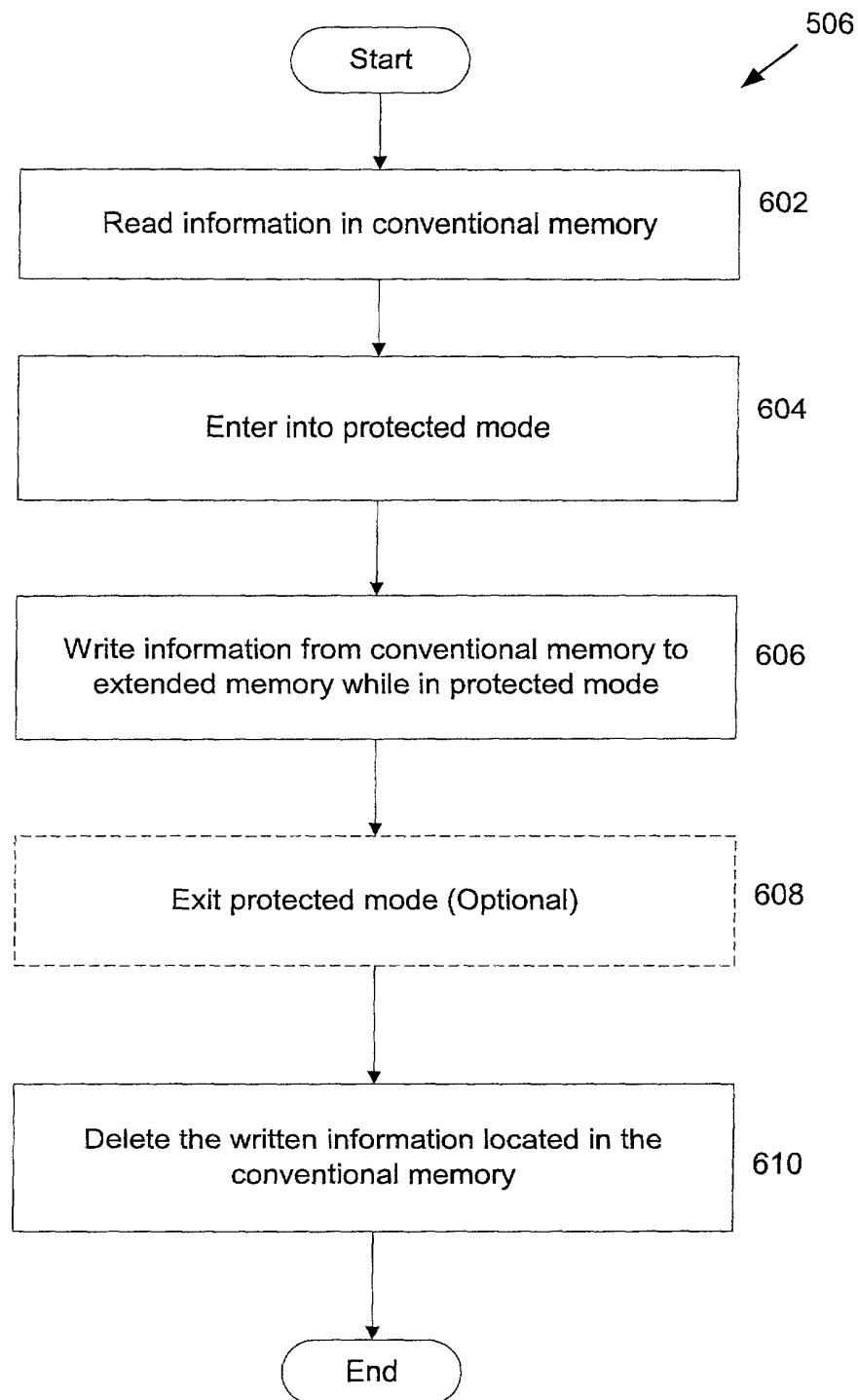


FIG. 7

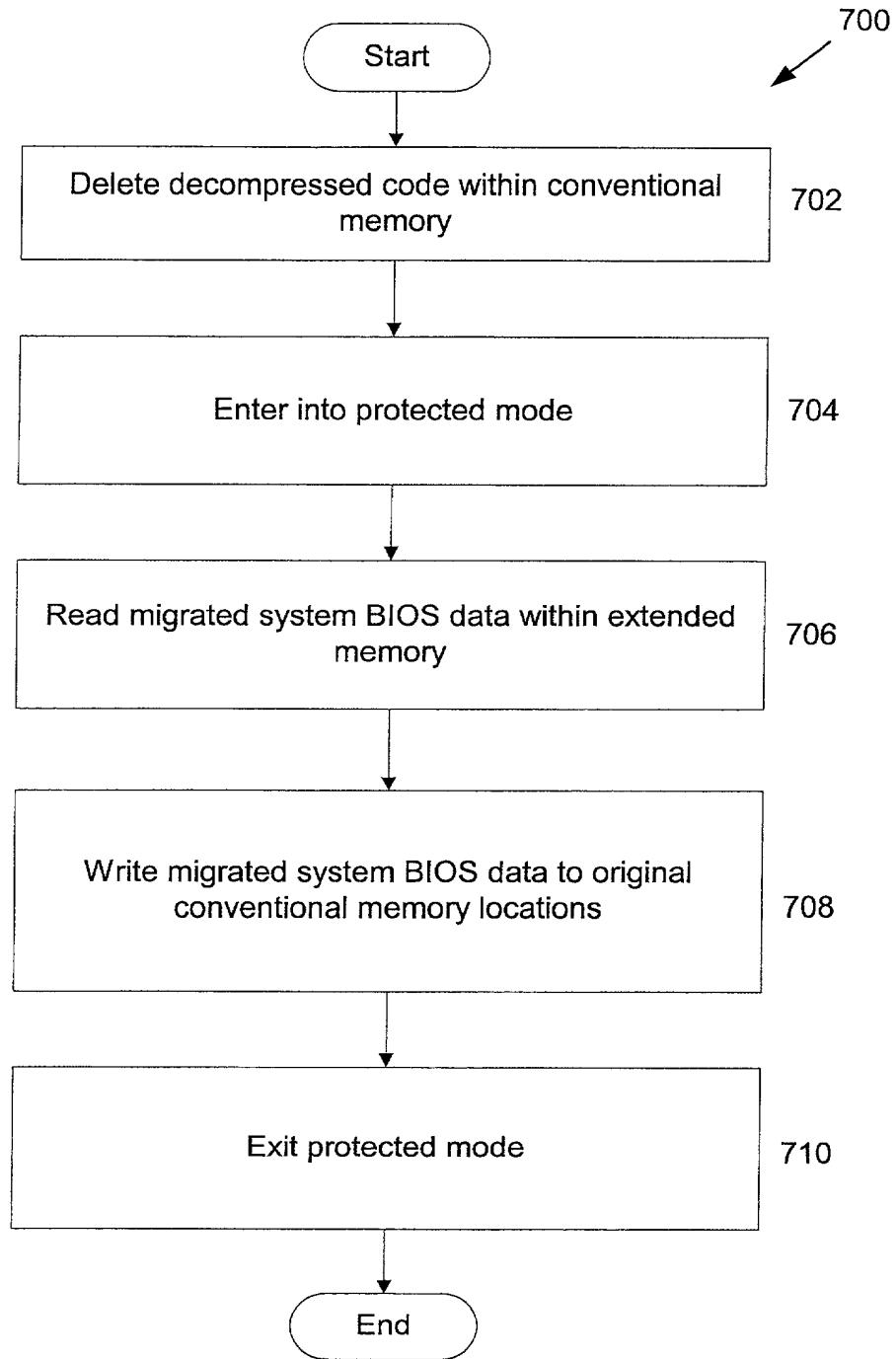


FIG. 8